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Description	



Distributed Joint Source-Channel Coding for Correlated Sources Using Non-systematic Repeat-Accumulate Based Codes

Valtteri Tervo · Tad Matsumoto · Pen-Shun Lu

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Abstract In this paper, we propose a technique for coding the data from multiple correlated binary sources, with the aim of providing an alternative solution to the correlated source compression problem. Using non-systematic repeat-accumulate based codes, it is possible to achieve compression which is close to the Slepian–Wolf bound without relying on massive puncturing. With the technique proposed in this paper, instead of puncturing, compression is achieved by increasing check node degrees. Hence, the code rate can be more flexibly adjusted with the proposed technique in comparison with the puncturing-based schemes. Furthermore, the technique is applied to distributed joint source-channel coding (DJSCC). It is shown that in many cases tested, the proposed scheme can achieve mutual information very close to one with the lower signal-to-noise power ratio than turbo and low density generator matrix based DJSCC in additive white Gaussian noise channel. The convergence property of the system is also evaluated via the extrinsic information transfer analysis.

Keywords Concatenated codes · Cooperative coding · Iterative decoding · EXIT chart

1 Introduction

Distributed source coding (DSC) and joint source-channel coding (JSCC) have been very active research areas in recent years. Lossy and lossless source coding for correlated sources is one of the hottest research topics nowadays. The Slepian–Wolf (SW) theorem [1] states that the joint entropy of the sources is achievable as long as decoding is performed jointly, while encoding can be performed independently. Conventional source coding under a distortion constraint and channel coding under the resource constraints of the channels, such as power and/or spectrum availability, have long been considered as the information-theoretic duals

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of each other, starting from Shannon's 1959 paper [2]. In [3], an exact characterization of the Shannon duality between data transmission and compression through Lagrange duality in convex optimization is provided. This suggests that excellent channel codes may also be excellent for the source compression.

In [4], A. Wyner realized the close connection of DSC to channel coding, and suggested the use of linear channel codes as a constructive approach to SW coding (SWC). Garcia-Frias et al. propose the use of turbo codes [5] for the compression of correlated binary sources in [6]. They applied the same structure for noisy channels in [7]. The use of linear codes with low density generator matrix (LDGM) for channel coding, compression, and JSCC of correlated sources was proposed in [8]. A more advanced low complexity cooperative source-channel coding scheme based on the proper use of LDGM code was proposed in [9]. In [10] a constructive approach for DSC of correlated binary sources using linear channel codes that can achieve any point of the SW region was proposed. A good tutorial for DSC for sensor networks is given in [11]. The applicability of DSC in video coding is investigated in [12, 13]. A universal approach to source controlled channel decoding was proposed in [14].

Most of the current practical SW codes, which are based on the use of channel codes, utilize puncturing for the rate adjustment. In this paper, we present an alternative technique to the puncturing based turbo compression. An advantageous point of the proposed technique over the conventional puncturing-based compression is that no information bits are lost, while they may be lost with the puncturing-based compression, of which category the conventional techniques belong to. Furthermore, we use our proposed technique to distributed JSCC (DJSCC) of correlated sources. We focus only on two-source symmetric scenarios, i.e., channels' statistical properties are identical for the two sources.

Repeat-accumulate (RA) [15] codes can be thought as a special case of low-density parity-check (LDPC) codes [16]. It has been proven in [17] that, in theory, RA codes can achieve the Shannon capacity in point-to-point additive white Gaussian noise (AWGN) channels. We utilize such powerful and flexible properties of RA codes for the compression of correlated sources and further apply the structure to DJSCC. The use of non-systematic code is beneficial because it can achieve arbitrary rate pairs of the signal to be transmitted, which is not always possible with systematic code. As described in [7], with turbo DJSCC, a half of the systematic bits are transmitted from the first encoder, and the other half from the second one. This explicitly indicates that one has to be very careful when determining the right puncturing pattern. Unlike the conventional approach, puncturing is not necessary in our proposed system. We set the average check node degree in RA code to be larger than the average variable node degree. In that way, we get the channel code rate larger than one and, hence, compression is achieved.

In order to understand the convergence properties of the system, we perform the extrinsic information transfer (EXIT) analysis [18] for our proposed system. This is motivated by the area property, which states that the area of the tunnel between the EXIT curves of the constituent decoders of a concatenated system is proportional to the gap between the source coding rate and the entropy.

The main contributions of this paper are summarized as follows. We propose a symmetric technique for coding the data from two correlated binary sources by using non-systematic RA based codes. The rate allocation can be performed via the node degree allocation and, hence, no puncturing is needed. Furthermore, we propose a doped accumulator (ACC) to shape the EXIT curves for better matching between them. Finally, we provide mathematical derivation for the relationship between the EXIT curves of the component decoders in the case that the code rate is smaller than or equal to 1.

The rest of this paper is organized as follows. Sections 2.1 and 2.2 describe the proposed system, with an emphasis on the structures of the encoder and decoder, respectively. Section 2.3 characterizes the doping technique used in [19]. In Sect. 3, the extrinsic information exchange property is analyzed theoretically. In Sect. 4, we demonstrate the performance of the system through EXIT chart analysis. Furthermore, we compare the performance of our proposed technique with counterpart techniques based on symmetric turbo code presented in [6,7] and LDGM code presented in [8]. Finally, Sect. 5 concludes this paper with some remarks.

2 Proposed System

In this section, the structures of the proposed encoder and decoder are described.

2.1 Encoder

The encoder structure is shown in Fig. 1. The encoder chain VNE– Π_s –CNE with s = 1, 2, where VNE stands for variable node encoder [20] and CNE stands for check node encoder [20], can be viewed as a special case of LDPC code. Since the standard LDPC code cannot achieve desirable performance in terms of compression when the correlation between the two sources is low, we attach ACC before the LDPC encoder. Furthermore, when the signal to noise power ratio (SNR) is low, we need to attach another ACC after the encoder to achieve better performance in terms of bit error ratio (BER). When the both ACCs are attached, the code is equivalent to accumulate-repeat-accumulate (ARA) code [21].

The information sequence $\mathbf{U}^{\mathbf{s}} = u_1^s, u_2^s, \ldots, u_L^s$ is optionally accumulated and *outer per-fect doping* [19] is used for the binary sequence output of the source. A pseudorandom doping sequence $\mathbf{D} = d_1, d_2, \ldots, d_{L(1-R_D)/R_D}$, where R_D ($R_D \leq 1.0$) is the code rate for doping and $d_k \in \{0, 1\}$, is placed pseudorandomly into the accumulated sequence resulting



Fig. 1 Transmitter side of the system model. Each source is encoded by an RA based encoder. The first ACC is used only when correlation is low and the second ACC is used when SNR is low. Encoders are enumerated as follows: $\{c_k^{s,1}\}$ represents the coded bits for source *s* after the first ACC and doping. $\{c_k^{s,2}\}$ are the coded bits for the source *s* associated to VNE. $\{c_k^{s,3}\}$ represents the coded bits for source *s* associated to encoder CNE+ACC which consist of a check node encoder and an accumulator. Π is a random interleaver of length L/R_D and Π_1 and Π_2 are random interleavers of length $\overline{d}_v L/R_D$

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in a sequence $\mathbf{C}^{s,1} = c_1^{s,1}, c_2^{s,1}, \dots, c_{L/R_D}^{s,1}$. **D** and its position indices in the vector $\mathbf{C}^{s,1}$ is assumed to be known to the decoder. The doping sequence **D** is used to improve the convergence properties of the system.

The correlated data is modeled according to the bit flipping model, i.e., $u_k^{2,1} = u_k^{1,1} \oplus e_k$ k = 1, 2, ..., L, where \oplus indicates the modulus 2 addition and e_k is a random variable, which takes value 1 with probability p and 0 with probability 1 - p. Both sequences U^s , s = 1, 2, are encoded independently each other by using non-systematic RA or ARA code with doping. The RA code consists of VNE, CNE and ACC. After the coding, binary phase shift keying (BPSK)-modulated symbols are sent trough the AWGN channel with the noise variance σ_n^2 .

A factor graph of ARA encoder with doping is depicted in Fig. 2. First of all, the source sequence \mathbf{U}^s is passed trough the ACC which consists of *L* check nodes. Note that all the output bits depends on all the bits preceding it. After the first ACC, the doping sequence \mathbf{D} is included and the resulting sequence is passed through L/R_D variable nodes. The resulting sequence is interleaved and provided to CNE which consists of check nodes. Finally, the sequence is accumulated again and passed forward to the BPSK modulator.

Let D_v be the number of different variable node degrees, and denote their degrees by $\tilde{d}_{v,i}$, $i = 1, ..., D_v$. The average variable node degree is calculated as [20]

$$\overline{d}_{\mathbf{v}} = \sum_{i=1}^{D_{\mathbf{v}}} a_{\mathbf{v},i} \tilde{d}_{\mathbf{v},i},\tag{1}$$

where $a_{v,i}$ is the fraction of nodes having degree $\tilde{d}_{v,i}$. In the same way, average check node degree is calculated as [20]



Fig. 2 Factor graph of non-systematic ARA encoder with doping

$$\overline{d}_{c} = \sum_{i=1}^{D_{c}} a_{c,i} \widetilde{d}_{c,i}.$$
(2)

The overall code rate of the ARA code is

$$R_{\rm c}^{\rm ARA} = \frac{d_{\rm c}}{\overline{d}_{\rm v}}.$$
(3)

If $\overline{d}_c > \overline{d}_v$, then $R_c^{ARA} > 1$, and, hence, it achieves compression. The data can be reconstructed in the receiver with the help of a priori information provided by the other decoder.

When we use doping, the overall code rate becomes

$$R_{\rm c} = R_D \frac{d_{\rm c}}{d_{\rm v}},\tag{4}$$

and the compression is achieved when $\overline{d}_{c} > R_{D}\overline{d}_{v}$.

2.2 Decoder

The extrinsic information exchange takes place in the decoder as depicted in Fig. 3. f_c denotes the extrinsic information update function due to the correlation, which is given by

$$\lambda_{A}^{q,j} = f_{c}(\lambda_{E}^{s,j}) = \ln \frac{(1-p)e^{\lambda_{E}^{s,j}} + p}{(1-p) + pe^{\lambda_{E}^{s,j}}},$$
(5)

where $\lambda_E^{s,j}$ is the extrinsic log-likelihood ratio (LLR) of the *j*th bit provided by the decoder of the source *s*, and $\lambda_A^{q,j}$ is the a priori LLR for the decoder of the source *q*. $I_{A,C^{s,r}}$, s = 1, 2,r = 1, 2, 3, denotes the a priori information for the decoder *r* of the source *s*. $I_{E,C^{s,r}}$ denotes the extrinsic information provided by the decoder *r* of the source *s*. $I_{A',C^{s,r}}$ and $I_{E',C^{s,r}}$ correspond to $I_{A,C^{s,r}}$ and $I_{E,C^{s,r}}$, respectively, however, it should be noticed that the information is forwarded towards the opposite direction. $I_{A'_D}$ denotes the mutual information (MI) provided



Fig. 3 Decoder structure showing the exchange of extrinsic information for the proposed system. *g* denotes the EXIT function corresponding to the LLR addition $\lambda_{E',C1,2} + \lambda_{A'_D}$ or $\lambda_{E',C2,1} + \lambda_{A'_D}$. VND and CND denote variable node decoder and check node decoder, respectively. DeACC denotes de-accumulator that decodes the ACC coded data

as a result of *outer perfect doping* and *g* denotes the EXIT function corresponding to the LLR addition $\lambda_{E',\mathbf{C}^{1,2}} + \lambda_{A'_{\mathbf{D}}}$ or $\lambda_{E',\mathbf{C}^{2,1}} + \lambda_{A'_{\mathbf{D}}}$, where $\lambda_{A'_{\mathbf{D}}} = \lambda_{A'_{d_1}}, \lambda_{A'_{d_2}}, \dots, \lambda_{A'_{d_{L/R_{\mathbf{D}}}}}$ denotes the LLR sequence including the doped bits defined as

$$\lambda_{A'_{d_k}} = \begin{cases} \operatorname{sign}(c_k^{1,1}) \cdot m \text{ if } c_k^{1,1} \text{ is doped bit} \\ 0 & \operatorname{otherwise}, \end{cases}$$
(6)

where *m* is a pre-set large value. All the numerical calculations are performed in the LLR domain to avoid the numerical instability and to increase the range of the likelihood values.

Note that there are two inputs into the decoder chain. To identify that information can be exchanged properly according to the turbo principle we need to perform the EXIT analysis. The analytical derivation of the relationship between the EXIT curves of the component decoders are presented in Sect. 3.

The factor graph of non-systematic ARA decoder with doping is depicted in Fig. 4. The factor graph of the decoder is actually a reverse-version of the encoder's factor graph. Note that, without doping, some of the check nodes has to have degree 1 [22]. Otherwise, the convergence does not start, because there is zero a priori information to be provided for the variable node decoder; this is due to the fact that if one of the check node inputs is zero, the check node output is also zero.

2.3 Reshaping the Accumulator EXIT Curve

In order to enhance the convergence, *inner systematic doping* [19] is used for the outer ACC. To see the difference between *inner systematic doping* and *outer perfect doping* reader may check [19]. This mechanism is demonstrated in Fig. 5. The principle is to transmit



Fig. 4 Factor graph of non-systematic ARA decoder with doping



Fig. 5 Factor graph of the doped accumulator



Fig. 6 EXIT chart for accumulator doping with different *P* values. The ratio between the noise and the energy per generated source bit $E_{so}/N_0 = 0.5$ dB, and the code rate of the RA code is $R_c^{RA} = 0.5$

ACC-coded bits in part, but in the other part directly from the check node output without ACC. With the technique described above, the shape of the EXIT function can be flexibly controlled by choosing the *P* value, for which the length *L* sequence is divided to *n* blocks, so that nP = L. Only every *P*th bit is taken from the accumulator output and the others are taken without accumulation. The effect of this method in terms of the shape of the EXIT curve is presented in Fig. 6. It can be seen that when *P* increases, MI increases on the left most part of the EXIT curve. Note that because the code rate does not change, the area property [23] justifies the fact that the right most part of the EXIT curve is lower when *P* increases.

3 Extrinsic Information Transfer Characteristic

In order to perform the EXIT analysis for the system we need to identify the relationship between the extrinsic MIs $I_{E,C^{1,1}}$ and $I_{E,C^{2,1}}$, which also identifies the relationship between a priori MIs $I_{A',C^{1,1}}$ and $I_{A',C^{2,1}}$ due to the symmetry of the system, assumed. Now, we can write the following two EXIT functions:

$$I_{E,\mathbf{C}^{2,1}}^{(l)} = T_{\mathbf{C}^{2,1}} \left(I_{A,\mathbf{C}^{2,1}}^{(l)}, I_{A',\mathbf{C}^{2,1}}^{(l-1)} \right),\tag{7}$$

$$I_{A,\mathbf{C}^{2,1}}^{(l)} = g^{-1} \left(I_{E,\mathbf{C}^{2,2}}^{(l)}, I_{A_{\mathbf{D}}'} \right) = g^{-1} \left(T_{\mathbf{C}^{2,2}} \left(I_{A',\mathbf{C}^{2,2}}^{(l-1)}, I_{A,\mathbf{C}^{2,2}}^{(l)} \right), I_{A_{\mathbf{D}}'} \right),$$
(8)

where *l* is the iteration index, *g* denotes the EXIT function representing the effect of the use of doping and $I_{A_{D}}$ indicates the a posteriori MI obtained from the doped bits introduced in Sect. 2.1. Because the MI does not change after interleaving or deinterleaving, following equivalence holds

$$I_{A,\mathbf{C}^{2,2}}^{(l)} = I_{E,\mathbf{C}^{2,3}}^{(l)} = T_{\mathbf{C}^{2,3}} \left(I_{A,\mathbf{C}^{2,3}}, I_{E',\mathbf{C}^{2,2}}^{(l-1)} \right), \tag{9}$$

It can be obtained from Fig. 3 that

$$I_{E',\mathbf{C}^{2,2}}^{(l-1)} = T_{\mathbf{C}^{2,2}}^{\prime} \left(g\left(I_{E',\mathbf{C}^{2,1}}^{(l-1)}, I_{A'_{\mathbf{D}}} \right), I_{A,\mathbf{C}^{2,2}}^{(l-2)} \right)$$
(10)

and

$$I_{E',\mathbf{C}^{2,1}}^{(l-1)} = T_{\mathbf{C}^{2,1}}^{\prime} \left(F_c \left(I_{E,\mathbf{C}^{1,1}}^{(l-1)} \right), I_{A,\mathbf{C}^{2,1}}^{(l-2)} \right),$$
(11)

where F_c represents the EXIT function corresponding to (5). Substituting (8), (9), (10) and (11) into (7) we know that the extrinsic MI $I_{E,C^{2,1}}$ depends only on the MI $I_{E,C^{1,1}}$, channel SNR, bit flipping parameter p and the doping pattern **D**. This can be expressed as

$$I_{E,\mathbf{C}^{2,1}} = \tilde{T}_{\mathbf{C}^{2,1}}(I_{E,\mathbf{C}^{1,1}}, E_{so}/N_0, p, \mathbf{D}),$$
(12)

where E_{so}/N_0 denotes the ratio between the energy per generated source bit and the noise power.

The extrinsic MI of VND, $I_{E, \mathbb{C}^{5,2}}$, is actually a sum of several repetition code outputs. The number of different repetition codes equals the number of different variable node degrees, D_{v} . Therefore, the output of the VND can be calculated as

$$I_{E,\mathbf{C}^{s,2}} = \sum_{i=1}^{D_{v}} a_{v,i} I_{E,\mathbf{C}_{i}^{s,2}},$$
(13)

where $I_{E, \mathbf{C}_i^{s,2}}$ denotes the extrinsic information provided by the repetition code *i*. Due to the *central limit theorem* [24], we can approximate the output LLR distribution as a mixture of D_v Gaussian distributions, where every component has a variance $\tilde{d}_{v,i}\sigma_v^2$, whith σ_v^2 denoting the variance of a priori LLRs $\lambda_{A, C^{s,2}}$. Hence, we can conclude that the distribution of the extrinsic LLRs provided by VND can be expressed as

$$\lambda_{E,\mathbf{C}^{s,2}} \sim \sum_{i=1}^{D_{\mathbf{v}}} a_{\mathbf{v},1} \mathcal{N}\left(\frac{\tilde{d}_{\mathbf{v},i}\sigma_{\mathbf{v}}^2}{2}, \tilde{d}_{\mathbf{v},i}\sigma_{\mathbf{v}}^2\right).$$
(14)

However, this is true only for the codes which have $R_C^{ARA} \leq 1$. If $R_C^{ARA} > 1$, the probability distribution of $\lambda_{E,C^{s,2}}$ has very high peak at the point where LLR is zero. This happens because as previously mentioned, the check node emits zero if one of the inputs is zero. This breaks the Gaussianity assumption.

4 Simulation Results

In this section, we show the performance of the system for two correlated sources with p as a parameter, which is assumed to be known to the receiver. We will first of all show the EXIT characteristics of the system in high SNR value range. To demonstrate the proper operability of the system as a DJSCC, we then show the performance with low SNR. Simulation parameters are summarized in Table 1. Parameter R denotes the total rate needed to transmit the two sources with arbitrarily low probability of error and E_{so} is the energy per source bit. N_0 is zero mean white Gaussian noise with variance σ_n^2 , which is assumed to be the same in both channels.

4.1 Correlated Source Compression

Because the correlation between $\mathbf{U}^1 = u_1^1, u_2^1 \dots$ and $\mathbf{U}^2 = u_1^2, u_2^2 \dots$ is modeled by bit flipping, the SW theorem states that the two sources can be compressed up to the joint entropy $H(\mathbf{U}^1, \mathbf{U}^2) = 1 + H(p)$. In this section, we assume $E_{so}/N_0 = 10$ dB.

The SW sum rate bounds for several p values are shown in Table 2, together with the sum rates required to achieve a point very close to the (1, 1) MI point in the EXIT chart by using the proposed method. Performance of turbo code-based technique is also presented for comparison. Only symmetric cases were assumed, i.e., $R_1 = R_2$. The rates were deter-

р	R_D	$d_{\rm v}$	dc	ac	Р	L	R
(a) Simu	lation para	meters for RA	A based compression				
0.2	0	2	[1 2 3]	[0.14 0.56 0.3]	1	250,000	1.85
0.1	9/10	2	[1 3]	[0.1 0.9]	200	225,000	1.59
0.05	9/10	2	[1 3 4]	[0.1 0.65 0.25]	0	225,000	1.46
0.025	9/10	2	[1 3 4]	[0.1 0.55 0.35]	0	225,000	1.41
p	R_D	$a_{\rm V}$	L	$(E_{so}/N_0)_{\rm prop}$			

Table 1 Simulation parameters

(b) Simulation parameters for RA based DJSCC. $R_c = 1/3$,

 $d_{\rm c} = [1 \ 2 \ 3], a_{\rm c} = [0.2 \ 0.2 \ 0.6], d_{\rm v} = [2 \ 4 \ 13], and P = 0$ for every p

0.2	9/10	[0.2 0.48 0.32]	225,000	-0.7
0.1	4/5	[0.29 0.45 0.26]	200,000	-1.4
0.05	4/5	[0.29 0.45 0.26]	200,000	-1.9
0.025	4/5	[0.29 0.45 0.26]	200,000	-2.1
0.01	1/2	[0.65 0.25 0.1]	125,000	-2.5

(c) Simulation parameters for RA based DJSCC. $R_c = 0.475$, $d_c=[1\ 2\ 3]$, $a_c=[0.2\ 0.2\ 0.6]$, $d_v=[2\ 4\ 13]$, and P=0 for every p

0.2	9/10	[0.31 0.56 0.13]	216,000	-0.4
0.1	4/5	[0.34 0.58 0.08]	192,000	-1.2
0.05	4/5	[0.34 0.58 0.08]	192,000	-1.8
0.025	4/5	[0.34 0.58 0.08]	192,000	-2.2
0.01	1/2	[0.85 0.125 0.025]	120,000	-2.2

0.025	0.05	0.1	0.2
1.17	1.29	1.47	1.72
1.41	1.46	1.59	1.85
1.31	1.435	1.63	1.89
	0.025 1.17 1.41 1.31	0.025 0.05 1.17 1.29 1.41 1.46 1.31 1.435	0.025 0.05 0.1 1.17 1.29 1.47 1.41 1.46 1.59 1.31 1.435 1.63

Table 2 The performance of the proposed method with different *p*-values and their corresponding SW limits



Fig. 7 EXIT trajectories for the proposed compression scheme

mined by adjusting the R_D and d_c values. It is found in Table 2 that the proposed system can achieve high compression rate, very close to the SW bound, when the correlation is low. The trajectories shown in Fig. 7 verify that the proposed system achieves a point very close to the (1, 1) MI point in the EXIT chart with the parameter values shown in Table 1a.

4.2 Distributed Joint Source-Channel Coding

Since we limit our investigations only to 2-user symmetric systems, we assume that the energy per symbol in both channels is identical, and that $C_1 = C_2 = C$, where C_i is the capacity of the AWGN channel *i*. In the following, we will use the energy per source bit, E_{so} , which can be related to the energy per information bit, E_b , as well as the energy per symbol E_s , by [7]

$$2E_{so} = H(\mathbf{U}^1, \mathbf{U}^2)E_{\mathbf{b}} = (1/R_{c1} + 1/R_{c2})E_{\mathbf{s}},$$
(15)

where R_{ci} is the overall code rate of the encoder for the source *i*, with *i* = 1, 2. Since we consider the symmetric case only, $R_{c1} = R_{c2} = R_c$. Hence, with the condition described above,

$$R < C = \frac{1}{2}\log_2(1 + SNR),$$
(16)



Fig. 8 The gap with respect to the theoretical limit in dB

has to be satisfied. Equation (16) is equivalent to

$$SNR > 2^{2R} - 1,$$
 (17)

which leads to the energy per generated source bit to noise power ratio

$$\frac{E_{so}}{N_0} > \frac{1}{2R_c} \frac{E_s}{N_0} = \frac{1}{2R_c} (2^{2R} - 1) = \frac{2^{2R} - 1}{2R_c}.$$
(18)

Furthermore, the overall information rate can be calculated as

$$R = \frac{H(\mathbf{U}^1, \mathbf{U}^2)R_c}{2} \quad information \ bits \ per \ channel \ use. \tag{19}$$

Figure 8 shows the E_{so}/N_0 gap in dB between the theoretical limit and the threshold E_{so}/N_0 value at which the proposed method achieves the opening of the convergence tunnel until a point very close to the (1.0, 1.0) MI point. Simulations were conducted with various values of p for $R_c = 1/3$ and $R_c = 0.475$. Furthermore, the performances with the symmetric turbo and LDGM based DJSCC are also plotted for comparison. It can be seen that when $R_c = 1/3$, p = 0.01 and p = 0.2, the threshold E_{so}/N_0 is lower with the proposed system than with the turbo DJSCC scheme. An interesting observation is that when $R_c = 1/3$, we can find a code for p = 0.01 which achieves the threshold E_{so}/N_0 closer to its theoretical limit than the code designed for p = 0.025. This is because we change R_D case by case. Furthermore, when $R_c = 0.475$ the proposed scheme can achieve the threshold E_{so}/N_0 value much closer to the theoretical limit than LDGM based code proposed in [8] for every value of p considered. Figures 9 and 10 presents the EXIT curves with different p values. We can see that the matching between the trajectories and EXIT curves is almost perfect, even



Fig. 9 EXIT chart for proposed DJSCC scheme with $R_c = 1/3$. Solid line without a marker is the trajectory and the lines with markers are the corresponding EXIT curves



Fig. 10 EXIT chart for proposed DJSCC scheme with $R_c = 0.475$. Solid line without a marker is the trajectory and the lines with markers are the corresponding EXIT curves

though slight discrepancies are still observed. This is because the EXIT curves presented here are actually projected EXIT charts of two-fold serially concatenated system, and hence drawing the EXIT curves requires chain simulations for the parts which are concatenated but not mainly focused on [25].

5 Conclusion

We have proposed a technique for the compression of correlated sources, as an alternative solution to the puncturing based turbo compression techniques. Each source is independently encoded using non-systematic RA based code with the average check node degree larger than the average variable node degree. An advantageous point of the proposed technique over the conventional puncturing-based compression is that no information bits are lost, while they may be lost with the puncturing-based compression, of which category the conventional techniques belong to. The proposed system was also applied to the DJSCC and the performance was compared with turbo and LDGM based DJSCC. It has been observed that the proposed system can achieve better performance in terms of threshold SNR, when the channel code rate is $R_c = 1/3$ and $R_c = 0.475$. This observation should hold also with other values of R_c . It should be noted as a concluding statement that extension to the scenarios having more than two sources is left as future study.

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