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Description	



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Electrical properties of nearly stoichiometric GaAs grown by molecular beam epitaxy at low temperature

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Nearly stoichiometeric GaAs epilayers doped with Si or Be were grown by molecular beam epitaxy at a low temperature with closely controlled fluxes near the stoichiometric conditions. Excess As point defects in the epilayers which acted as trap sites of free carriers were studied by means of the Hall effect measurements. Under the same As/Ga flux condition, concentrations of excess As point defects which trap free carriers increase with increase of concentrations of Si or Be. With the same concentration of the dopants, on the other hand, concentrations of excess As point defects increase linearly with the As/Ga flux ratio. The dependence of carrier mobility on the trapped carrier concentrations indicates that free carriers are trapped by neutral point defects and change them into ionized ones. In the epilayers which are very close to the stoichiometric composition, Si exhibits its amphoteric nature, occupying both donor and acceptor sites and results in the self-compensation. © 2001 American Institute of Physics. [DOI: 10.1063/1.1330765]

I. INTRODUCTION

GaAs epilayers grown at low temperatures (LT-GaAs) exhibit a number of novel properties such as high electrical resistivity and ultra-fast response to photoexcitation.^{1,2} These properties result from high concentrations of excess As in the epilayers. Among these properties, ultra-fast response to photoexcitation, that is, ultra-short lifetime of photoexcited carriers, is attracting a great deal of attention. In recent years LT-GaAs has been extensively studied, aimed at applications to optoelectronic devices such as THz electromagnetic pulse generators^{3–5} and also all-optical switches^{6–8} which will have a large impact on the future optoelectronic technology.

Studies on excess As point defects in LT-GaAs have been carried out so far mainly by means of spectroscopic techniques and theoretical calculations. The main excess As point defects are antisite As, As_{Ga} , and their concentrations have been measured by means of the near infrared absorption spectroscopy.^{9,10} Because of the linear correlation between the concentration of As_{Ga} and lattice expansion of LT-GaAs epilayers, almost all excess As are considered to be As_{Ga}.¹⁰ According to the theoretical study,¹¹ As_{Ga} is known to act as a double donor. Nearly one order of magnitude smaller concentrations of As_{Ga} were found to be singularly ionized in undoped LT-GaAs by means of the electron spin resonance spectroscopy.¹² In addition to As_{Ga}, the slow positron annihilation has shown the existence of Ga vacancies, V_{Ga} , in this material.¹³ The theoretical study indicates that V_{Ga} is a triple acceptor.¹⁴ Besides these point defects, existence of defect complexes and interstitial As atoms has been suggested,^{15,16} but their existence has not been experimentally confirmed without ambiguity. Even if they are present, their concentrations are considered to be much smaller than that of As_{Ga} . A recent study has shown that the concentration of singularly ionized As_{Ga} is three times the concentration of V_{Ga} in undoped LT-GaAs, implying that these point defects compensate each other.¹⁷

Relaxation processes of photoexcited carriers in LT-GaAs have been studied by means of the pump-probe method.^{18–25} According to these studies, photoexcited carriers are first trapped by excess As point defects in subpicoseconds and recombine in relatively long times in as-grown LT-GaAs. It is suggested that photoexcited electrons are trapped by positively ionized As_{Ga} and holes are trapped by negatively ionized V_{Ga}. In annealed LT-GaAs, on the other hand, metallic clusters of excess As act as recombination centers of photoexcited carriers.¹⁹

In order to have improvement of the properties of LT-GaAs as THz electromagnetic pulse generators and to realize all-optical switches based on LT-GaAs, it is necessary to have further understanding of excess As point defects, which will enable one to develop new methods for control of their concentrations and spatial distribution. In the study reported in this article, we have investigated excess As point defects by utilizing a method different from those employed up to the present for the studies of LT-GaAs. Nearly stoichiometric LT-GaAs epilayers were grown with donor or acceptor dopants, and the dependence of their electrical properties on the concentration of excess As point defects were studied by Hall effect measurements. From this study, new insights of excess As point defects which trap charge carriers have been obtained. The results also clarify new fundamental aspects of low temperature molecular beam epitaxy (MBE) growth of compound semiconductors.

In earlier studies, O'Hagan *et al.*, have showed that the concentration of excess As in LT-GaAs can be considerably reduced by lowering the As flux with respect the Ga flux and the conduction by free carriers was observed in the doped epilayers grown under such flux conditions.²⁶ In recent stud-

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ies, Suda and Otsuka have investigated the dependence of the concentration of excess As on the As/Ga flux ratio for different growth temperatures and made the following two findings.^{27,28} The first finding is that one can obtain stoichiometric LT-GaAs epilayers regardless of the growth temperature under the flux condition where the As/Ga flux ratio corresponds to the number ratio of As₄ molecules and Ga atoms in the dissociation reaction proposed by Foxon and Joyce.²⁹ The other is that the dependence of the concentration of excess As on the As/Ga flux ratio at a fixed growth temperature is described in terms of the Langmuir adsorption isotherm and, hence, the concentration can be closely controlled by the flux condition. In the present study, based on these results, the concentration of excess As was systematically varied in the vicinity of the stoichiometric composition, and the concentration and mobility of free carriers were measured as a function of the concentration of excess As.

II. EXPERIMENT

A. MBE growth

LT-GaAs epilayers were grown by utilizing a conventional molecular beam epitaxy (MBE) system. The background pressure of the growth chamber was less than 5×10^{-10} Torr, and semi-insulating epi-ready (100)GaAs wafers were used as substrates. For each growth experiment, a substrate which was cut into a size of 18×15 mm² was mounted on a Mo holder with indium. After desorption of a oxide layer of the substrate surface, the surface was annealed with the As flux for 10 min at 600 °C, followed by the growth of a GaAs buffer layer of a thickness of 150 nm at 580 °C. After the growth of the buffer layer, the temperature of the sample was lowered to 270 °C at which all LT-GaAs epilayers were grown in this study.

The temperature of the sample surface was measured by using a quartz rod connected to a pyrometer before and after the growth of each LT-GaAs epilayer. Details of the method of this temperature measurement was described in an earlier report.²⁷ The reason for the choice of 270 °C as the growth temperature of LT-GaAs epilayers is that the dependence of the concentration of excess As on the As/Ga flux ratio at this temperature is given as the simplest form of the Langmuir adsorption isotherm:

$$[As^{ex}] = \frac{J_{As}/J_{Ga} - 1}{J_{As}/J_{Ga}} [As^{ex}]_{sat}, \qquad (1)$$

where $[As^{ex}]$ and $[As^{ex}]_{sat}$ are the concentration of excess As for the As flux J_{As} and Ga flux J_{Ga} and the saturated concentration of excess As at the growth temperature 270 °C, respectively.²⁸ In order to obtain a stable As flux during the growth, the As effusion cell was maintained at a given temperature up to 2 h prior to the growth. All LT-GaAs epilayers were grown for 40 min, and the Ga flux was maintained at a constant ion gauge reading, $(5.2\pm0.1)\times10^{-7}$ Torr, which gives rise to a thickness of the epilayers, 600 nm. The As flux was varied for LT-GaAs epilayer grown in this study in the range of ion gauge readings between 6.5×10^{-6} and 1.1 $\times10^{-5}$ Torr. The readings of As fluxes after the growth did not change from those before the growth by more than 2%.



FIG. 1. X-ray diffraction rocking curves of 400 reflections taken from two points of a LT-GaAs epilayer. Two points are apart from each other by 6 mm in the direction of the change of the flux ratio with the one (a) being on the higher flux ratio side and the other (b) on the lower flux side.

Si was doped as a donor dopant with concentrations of either 1.58×10^{18} or 3.40×10^{18} cm⁻³, and Be was doped as an acceptor dopant with concentrations of either 5.44×10^{18} or 9.44×10^{18} cm⁻³. The concentrations of the dopants were calibrated by measuring carrier concentrations in doped GaAs epilayers grown at 580 °C where nearly all dopants provide free carriers at room temperature.

In all growth experiments, the substrate holder was not rotated during the growth, and substrates were positioned in such a way that a slight change in the flux ratio was intentionally introduced along the longer edge of a substrate. In this way a number of data points of the dependence of the electrical property on the concentration of excess As were obtained from one sample. Figure 1 are x-ray 400 rocking curves measured from two points (a) and (b) of a low Bedoped epilayer which was grown with a Ga flux, 5.2 $\times 10^{-7}$ Torr, and As flux, 8.2×10^{-6} Torr. The two points were apart from each other by 6 mm along the longer edge. From these rocking curves, the changes of lattice spacings of LT-GaAs epilayers, $\delta d/d$, were determined, and then corresponding flux ratios at two points were estimated by using the equation derived by Suda and Otsuka,²⁸

$$\left(\frac{\delta d}{d}\right) = \frac{J_{\rm As}/J_{\rm Ga} - 1}{J_{\rm As}/J_{\rm Ga}} \left(\frac{\delta d}{d}\right)_{\rm sat},\tag{2}$$

where $(\delta d/d)_{\text{sat}}$ is the saturation value at 270 °C, being 0.705×10^{-3} . The flux ratios derived from the two points (a) and (b) are 1.644 and 1.520, respectively, which gives rise to a gradient of the flux ratio along the sample surface of 0.0206/mm with the assumption of a linear change of the flux ratio. From the epilayer which was grown with a higher As flux, a similar value of the change of the flux ratio was obtained. The change of the flux ratio over the epilayer surface seems to have resulted mainly from a nonuniformity of the As flux because the thickness of the epilayer was nearly constant over the epilayer according to the fringe spacings in the x-ray rocking curves.

For epilayers which are closer to the stoichiometric composition, $\delta d/d$ cannot be accurately measured from x-ray rocking curves, and, hence, a flux ratio at each point of the epilayers was estimated in the following method. The ion gauge reading is considered to represent the average As flux over the epilayer surface, and, therefore, it is considered to correspond to the As flux at the center of the surface. By comparing an ion gauge reading of an epilayer with the ion gauge reading of the reference epilayer whose x-ray rocking curves are shown in Fig. 1, the flux ratio at the center of the former epilayer was determined. Next, flux ratios at other points of the epilayer surface were estimated by using the above-mentioned gradient of the flux ratio, that is, 0.0206/ mm. For the epilayers grown in this study, the flux ratio $J_{\rm As}/J_{\rm Ga}$ ranges from 0.92 to 1.70. In the case that a flux ratio becomes less than 1.0, the surface always appear nonmirrorlike, indicating that the above-mentioned method for assigning the flux ratio to a given point of an epilayer surface is a reasonable one. In order to examine the accuracy of the above-mentioned method of determination of a flux ratio at each point of an epilayer surface, two epilayers were grown by rotating the substrate holder during the growth. The ion gauge readings of As fluxes of the two epilayers were 5.9 $\times 10^{-6}$ and 7.6 $\times 10^{-6}$ Torr, corresponding to flux ratios 0.967 and 1.338, respectively. The x-ray rocking curves obtained from these rotated epilayers closely match those obtained from two points of the unrotated epilayer whose flux ratios are considered to be the same as the above two values according to the assumption of the linear change of the As flux along the epilaver surface. The surface of the rotated epilayer grown with the flux ratio 0.967 was also found to be nonmirrorlike as in the case of unrotated epilayers.

B. Hall effect measurement

All epilayers grown in the study were examined by the Hall effect measurements with the van der Pouw method. The samples for the measurements were made in the following way. From all epilayers, a set of three square pieces with a size of 5×5 mm² were cut in the direction of the increase of the flux ratio. For the epilayers which are very close to the stoichiometric composition, a set of smaller pieces with a size of 2×2 mm² were obtained from the region of the non-mirrorlike surface to that of the mirrorlike surface. Indium contacts were made at corners of each piece, and the piece was annealed at 270 °C from 10 to 30 min in a nitrogen atmosphere in order to obtain ohmic contacts. The Hall effect measurements were made at room temperature.

Figures 2(a)-2(c) are the results of Si-doped epilayers. Figure 2(a) shows the dependence of the charge carrier concentration on the flux ratio. For both high doping and low doping, the carrier concentration monotonously increases as the flux ratio decreases towards 1.0. The carrier concentration, however, reaches maxima around the flux ratio 1.04 and sharply decreases at lower flux ratios. This sharp decrease of the carrier concentration starts at the location where the surface is still mirrorlike. The conduction type in this low carrier concentration region is the *p* type, unlike other regions which exhibit the *n* type conduction. Figure 2(b) is the de-



FIG. 2. Results of Hall effect measurements of samples cut from Si-doped LT-GaAs epilayers which were grown under different flux ratios: (a) charge carrier concentrations, (b) mobility, and (c) trapped carrier concentrations.

pendence of the mobility on the flux ratio. For both doping conditions, the mobility increases with the decrease of the flux ratio except for the range from nearly 1.0 to the lower ratio. By comparing this dependence with that of Fig. 2(a), one can find that the mobility increases with the increase of the carrier concentration in the present case.

Figure 2(c) shows the trapped carrier concentration as a function of the flux ratio. The trapped carrier concentration is derived by subtracting the free carrier concentration from the dopant concentration. For both high doping and low doping, the figure shows a nearly linear increase of the trapped car-



FIG. 3. Results of Hall effect measurements of samples cut from Be-doped LT-GaAs epilayers which were grown under different flux ratios: (a) charge carrier concentrations, (b) mobility, and (c) trapped carrier concentrations.

rier concentration from the stoichiometric value to 1.6, beyond which the concentration remains nearly constant at the values of the dopant concentrations. Besides the linear increase of the trapped carrier concentration, the figure shows another important point. In the range where the concentration linearly increases, values of trapped carrier concentrations of high doping and low doping are significantly different from each other at the same flux ratio; the trapped carrier concentration of high doping is much greater than that of low doping at a given flux ratio.

Figures 3(a)-3(c) are the results of Be-doped epilayers. The carrier concentration, mobility and trapped carrier concentration exhibit similar trends to those of Si-doped epilayers in the region of the flux ratio less than 1.1 where carrier concentrations are on the order of 10^{18} cm⁻³. These trends are monotonous increase of the carrier concentration towards the unity flux ratio, the increase of the mobility with the

increase of the carrier concentration, the linear increase of the trapped carrier concentration with the increase of the flux ratio from the unity flux ratio, and different trapped carrier concentrations at a given flux ratio for high doping and low doping. There is, however, one difference from the case of the Si-doped epilayers. In this case the carrier concentration remains high even for the flux ratio below 1.0, and the conduction type remains as the p type throughout this range including the region where the surface is nonmirrorlike. There is another interesting point for the Be-doped epilayers. In the higher flux ratio range where the trapped carrier concentrations saturate, the conduction type has changed into the n type, and the carrier concentration gradually increases with the increase of the flux ratio, especially in the case of high doping.

III. DISCUSSION

As mentioned in the Introduction, among a number of excess As point defects, existence of As_{Ga} and V_{Ga} have been experimentally confirmed to date without ambiguity. In this article, therefore, the results of the study are discussed by referring to these two types of point defects. Antisite As, As_{Ga}, is known to act as a deep donor, and V_{Ga} as a deep acceptor. Hence, the former is expected to trap a hole provided by Be, and the latter is expected to trap an electron given by Si. The results presented in the previous section are reasonably explained with this picture. It has been shown that the majority of excess As point defects are considered to be As_{Ga} , while the concentration of V_{Ga} , on the other hand, is known to be one order of magnitude smaller than that of As_{Ga} .¹⁷ As seen in Figs. 2(c) and 3(c), trapped carrier concentrations in the Be-doped epilayers are nearly one order of magnitude greater than those in the Si-doped epilayers, indicating that holes are trapped by As_{Ga} and electrons by V_{Ga} .

The correlation of the mobility with the carrier concentration is also explained with this picture. The mobility of charge carriers in both Si-doped epilayers and Be-doped epilayers decrease with increasing trapped carrier concentrations in the region where free carriers are present. This implies that trapping of charge carriers results in ionizations of point defects and, hence, leads to larger scattering of charge carriers. Therefore, one can interpret the present results in such a way that holes are trapped by neutral As_{Ga}, resulting in positively ionized point defects, and electrons are trapped by neutral V_{Ga}, resulting in negatively ionized point defects. There is another result which fits well to this picture. In the Be-doped epilayers, the conduction type becomes the *n* type in the flux range where the trapped carrier concentration saturates, all free carriers being trapped, and charge carrier concentrations increase with the further increase of the As flux. In this range, nearly all As_{Ga} are positively ionized with a few remaining as neutral As_{Ga} . Electrons of neutral As_{Ga} , hence, can move to ionized As_{Ga}, resulting in the hopping conduction of electrons. With further increase of the As flux, the concentration of neutral As_{Ga} increases and results in the increase of the concentration of hopping electrons.

The linear increase of trapped carrier concentrations with the increase of the flux ratio from unity in both Sidoped and Be-doped epilayers is explained in terms of the Langmuir adsorption model proposed by Suda and Otsuka.²⁸ According to Eq. (1), the concentration of excess As linearly increases with the increase of the flux ratio from unity in the vicinity of the stoichiometric flux condition. The present results indicate that concentrations of both As_{Ga} and V_{Ga} change with the flux ratio according to this Langmuir adsorption model. The results also indicate that concentrations of these excess As point defects approach to approximately zero as the flux ratio moves to unity, except for a sudden increase around the stoichiometric flux condition in the case of Sidoped epilayers. Because of the intentional change of the flux ratio over an epilayer surface and a slight change of the As flux during the growth of a LT-GaAs epilayer, we did not obtain a LT-GaAs epilayer where no significant concentration of trapped carriers exists and, hence, a free carrier concentration is exactly equal to the dopant concentration. The present results, however, suggest that one could obtain a LT-GaAs epilayer close to such a condition if the fluxes were carefully controlled for the growth.

Figures 2(c) and 3(c) show that trapped carrier concentrations have increased with the increase of dopant concentrations for a given flux ratio. This means that more V_{Ga} are incorporated with a higher Si concentration and more AsGa with a higher Be concentration. Such a tendency has never been observed in earlier studies of LT-GaAs. A higher dopant concentration is obtained with a higher effusion cell temperature, which will result in a slightly higher epilaver surface temperature during the growth due to the thermal irradiation. The above-mentioned tendency of the incorporation of excess As point defects is opposite to that expected from this thermal effect. A more reasonable cause of the present results is the tendency of reducing free carrier concentrations in a semiconductor material, since the presence of a high concentration of free carriers implies that the electronic system of a semiconductor is in a higher energy state than that of an intrinsic semiconductor. In other words, an electronic system of a semiconductor has an intrinsic tendency to try to keep the Fermi level at the midgap. When a higher concentration of Si is doped, more V_{Ga}, which are deep acceptors, are incorporated for a given flux condition. Similarly, more As_{Ga}, which are deep donors, are incorporated when a higher concentration of Be is doped. It is interesting to note that such an intrinsic tendency of the electronic system of a semiconductor occurs even in the case of the low temperature growth of MBE where only surface atoms are movable via surface migration and desorption.

In earlier studies,³⁰ it was found that incorporation of a high concentration of dopants leads to significant reduction of the concentration of excess As even under the high As flux condition. The doping concentrations employed in these studies were on the order of 10^{19} cm⁻³, which are much higher than those employed in the present study. This effect of the dopant on the incorporation of excess As is opposite to the present case. We believe that the reduction of the concentration of excess As by the high concentration of dopants is explained in the following way. According to the Langmuir adsorption model for incorporation of excess As in LT-GaAs,²⁸ chemisorbed As atoms on the As terminated sur-

face of GaAs serve as a precursor of excess As atoms. Arrival of a high concentration of dopant atoms on such a surface is likely to disturb chemisorbed As atom layers and, hence, reduce the incorporation of excess As. In fact, the supply of Si atoms on the $c(4 \times 4)$ surface of GaAs is found to replace chemisorbed As atoms in scanning tunneling microscope (STM) studies,³¹ which supports the above explanation.

Another new finding made in this study is the sharp reduction of the free carrier concentration of Si-doped epilayers in the neighborhood of the stoichiometric flux condition. As stated earlier, this reduction has started in the region where the flux ratio was slightly higher than unity and the epilayer surface still has remained mirrorlike. In the region where the flux ratio is less than unity, the surface is nonmirrorlike and a high density of defects was observed by crosssectional transmission electron microscopy (TEM).³² In these defective regions, a significant concentration of charge carriers may be trapped by the defects, but the sharp reduction of the free carrier concentration in the mirrorlike surface region cannot be explained in this manner. Under a nearly stoichiometric flux condition at a low temperature, the growth surface is expected to have both As atom exposed and Ga atom exposed regions. It is, therefore, expected that the amphoteric dopant Si may be incorporated in both donor and acceptor sites, which results in self-compensation, tending to keep the Fermi level at the midgap. As a support for this explanation. Si is known to be incorporated as an acceptor in the liquid phase epitaxy (LPE) where the growth of a GaAs crystal is carried out in a Ga-rich atmosphere.³³ In a preliminary study by photoluminescence measurements of these Si-doped epilayers, we also found peaks corresponding to Si donors and Si acceptors. At the normal growth temperature of MBE, the growth surface is maintained as an Asstabilized one even if the resulting epilayer is stoichiometric. Hence, Si tends to be incorporated predominantly as a donor. The present result, therefore, suggests that the incorporation of Si as a donor in the MBE growth does not necessary result from an intrinsic nature of MBE.

In summary, the main results of the present study are described in the following.

(1) Under the same flux condition, the incorporation of donor dopant Si or acceptor dopant Be leads to the increase of a concentration of excess As point defects which act as charge carrier trap sites. This result is ascribed to the presence of free carriers in nearly stoichiometric epilayers and to the intrinsic tendency of the electronic system of a semiconductor to maintain the Fermi level close to the midgap.

(2) For a constant dopant concentration, the trapped carrier concentration linearly increases with the flux ratio from the stoichiometric condition. This linear change is expected from the Langmuir adsorption model for incorporation of excess As in LT-GaAs.

(3) Conduction electrons and holes which are supplied by doped impurities are trapped by neutral V_{Ga} and As_{Ga} , respectively, and change them into ionized point defects. These processes are indicated by the dependence of mobility on free carrier concentrations. (4) In the neighborhood of the stoichiometric composition of LT-GaAs, Si exhibits its amphoteric nature and leads to self-compensation.

These results provide new insights on excess As point defects in LT-GaAs, but also clarify new fundamental aspects of the MBE growth of compound semiconductors.

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