

Title	プロセス代数に基づくシステムレベル設計アーキテクチャ
Author(s)	岩政, 幹人
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Description	Supervisor: 日比野靖, 情報科学研究科, 博士

System level specification engineering is expected to be the vehicle for achievement of the automatic SOC synthesis. There still exist gaps between higher level specification and detail level one. For example, the UML sequence diagram only captures one aspect of the system's behavior. The synthesizing method of the whole system specification from the system specification fragments is sought for.

The authors show a framework for system level specification synthesis which uses process algebra as a formal basis and synthesizes the whole system specification from message sequence charts as system behavior scenarios by extending process algebraic operations.